

## Fermi National Accelerator Laboratory LDRD Project Data Sheet - FY16

**Project ID:** FNAL-LDRD-2016-010

**Project title:** Preparing HEP reconstruction and analysis software for exascale-era computing

**Principal investigator:** Marc Paterno

**Project description:** (short description and explanation of cutting edge, high-risk, high-potential science or engineering)

The project is to produce a prototype software system suitable for moving high energy physics (HEP) experiment event data through multiple processing stages in an exascale-class computing facility. There are two critical components to be demonstrated: a) high-performance I/O to a parallel filesystem and b) communication of event data through node interconnects rather than through the filesystem. Simulated and experimental HEP data will be used for the I/O implementation studies, the data store methods for a high-core-count, low-memory per core system, and studying the scaling performance.

**Tie to Mission:** (explain the project's relevance or anticipated benefits to Fermilab's and DOE's missions)

High energy physics experimentation requires advanced computing capabilities and often breakthroughs in experimentation are the result of advances in computing. Current HEP projects take advantage of "high throughput computing" (HTC) which utilize an architecture not compatible with new "high performance computing" (HPC) machines that will make available factors of 10s and 100s more processing power. This project will perform R&D on the architectures required for HEP data to make use of HPC machines.

**Previous year's accomplishments:** (as applicable) FY15, not applicable

**Work proposed for current fiscal year and anticipated / desired results:**

The first year of the project will focus on encoding HEP data into a HDF5 format that enables the use of parallel filesystems which is challenging in that HEP data is irregular. In addition, a model for distributing data will be developed where the challenge is to be able to store data that can efficiently communicate with all the compute nodes to produce the final products intended for storage via I/O nodes. Development will be conducted to make the internal communication efficient.

**Project funding profile:** (costs, budgets, projected budgets, and total)

Prior year(s) costs	FY16	FY17	FY18	Total
N/A	210,000	417,400	--	627,400

Project Start Date: 1/01/2016 Total Approved Project funds: \$ 627,400